

FIG. 1A

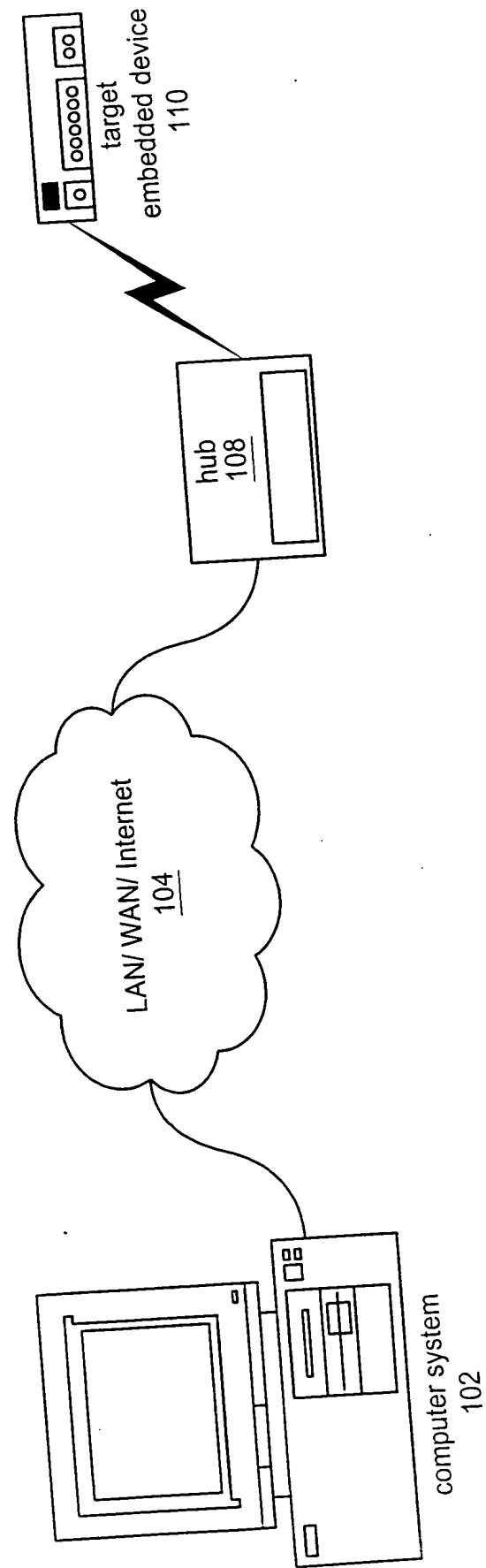


FIG. 1B

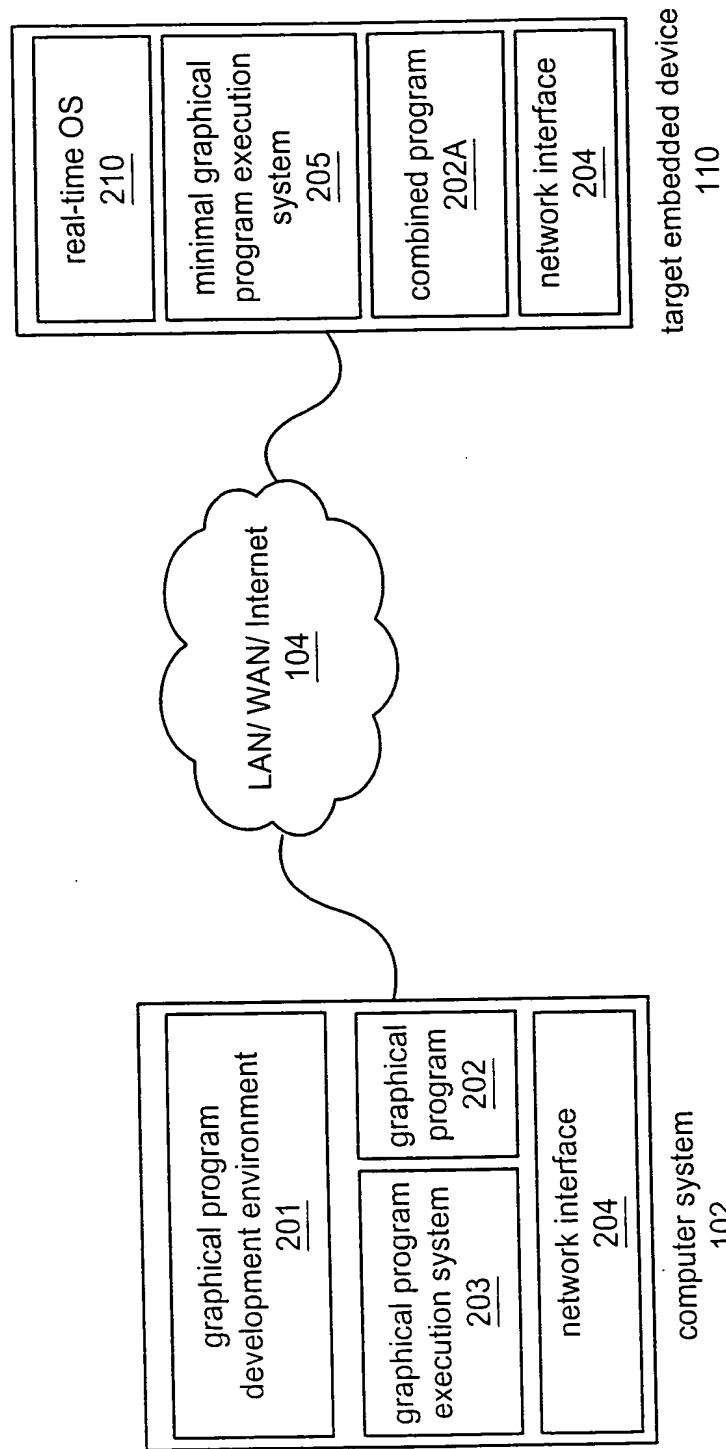


FIG. 2

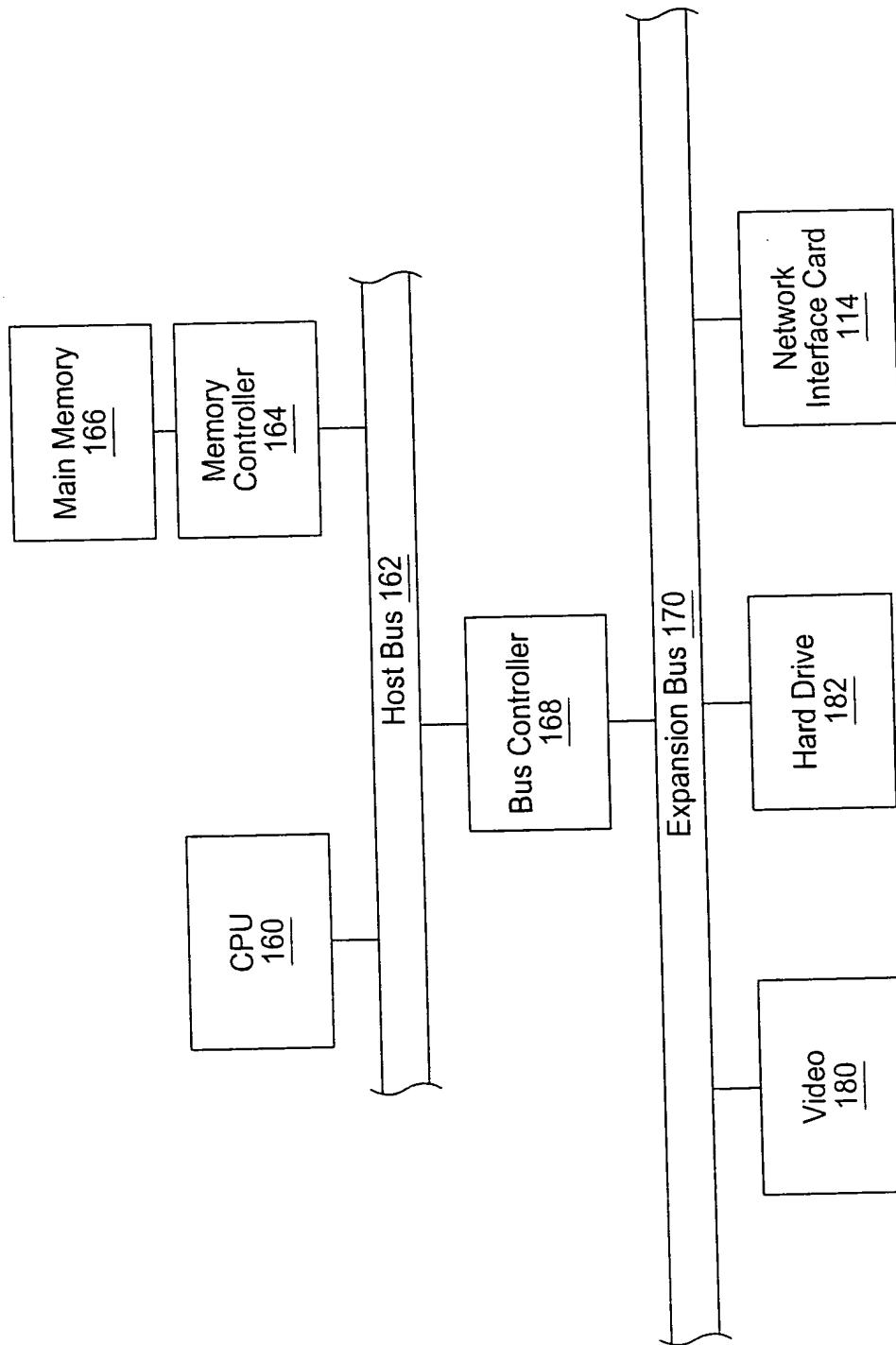


FIG. 3

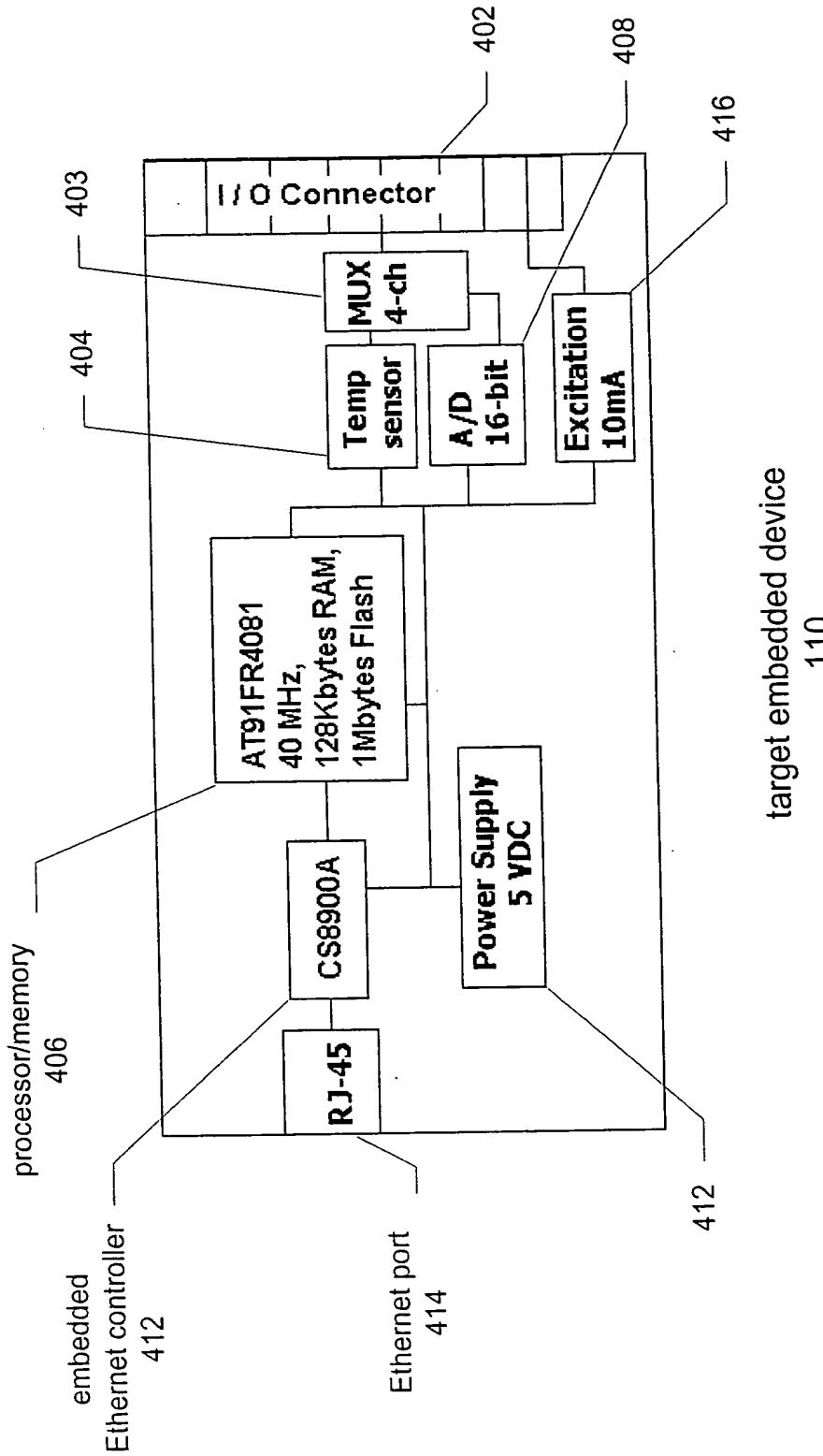
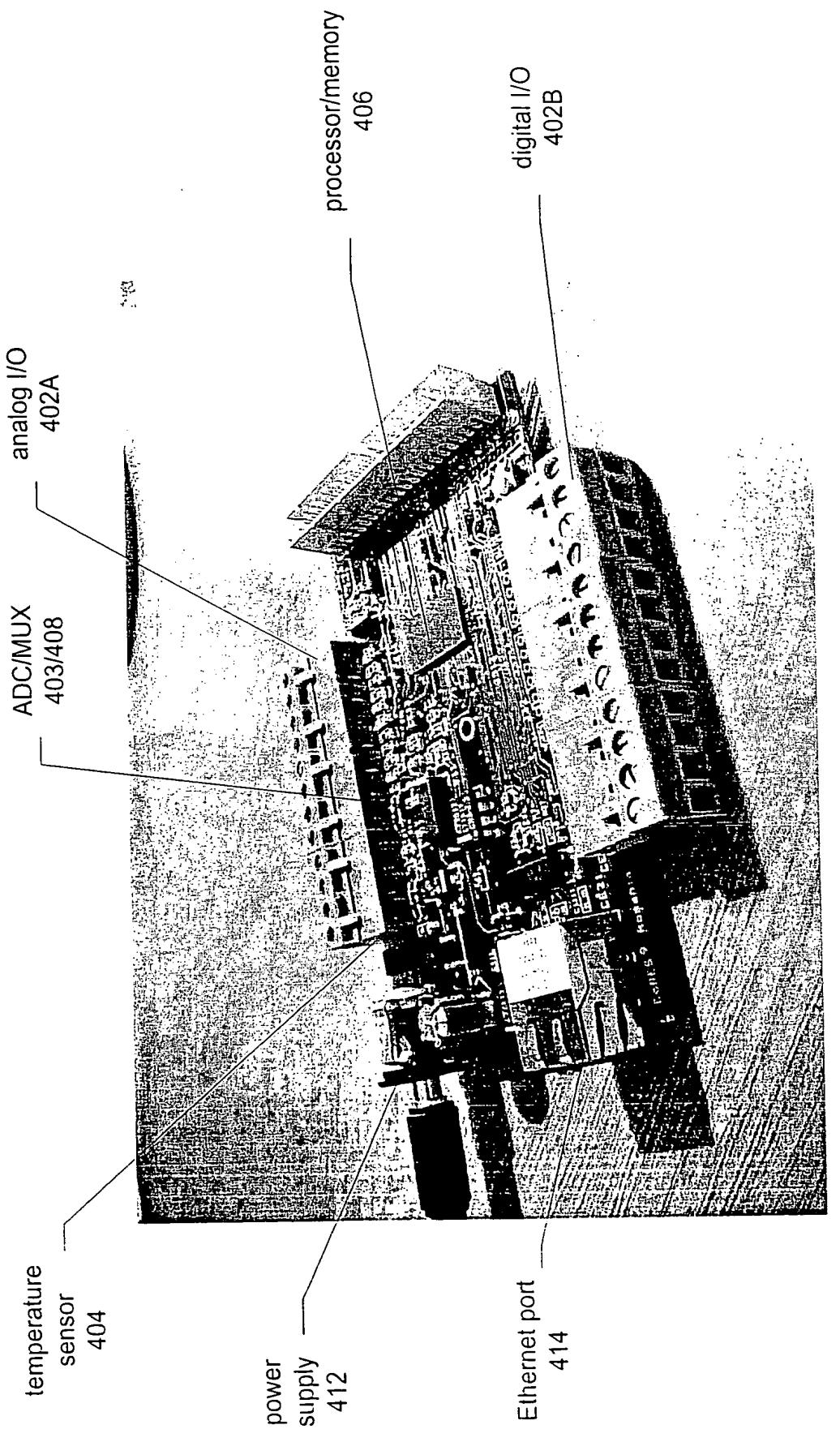


FIG. 4A



target embedded device  
110

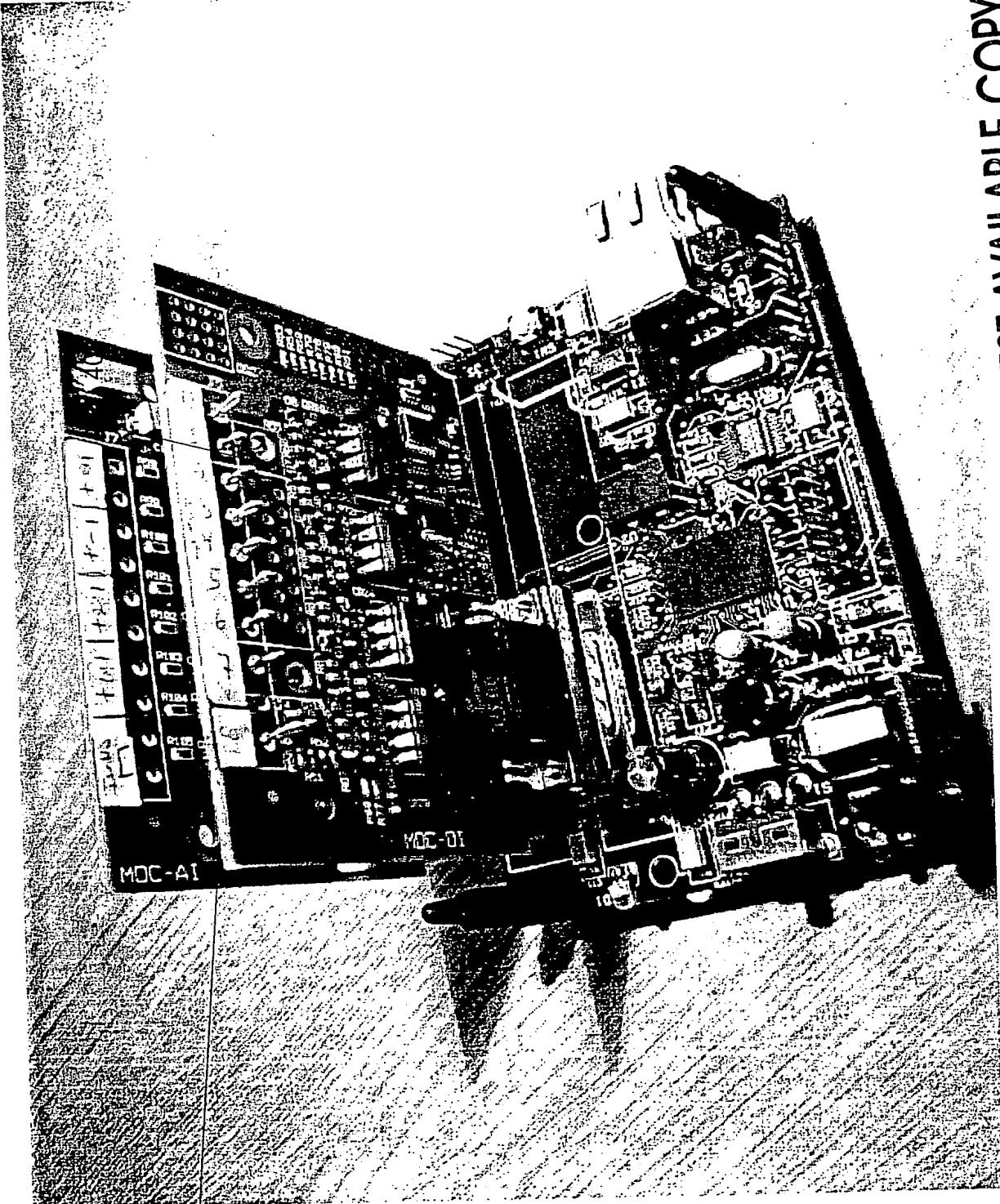
TEST AVAILABLE COPY

FIG. 4B

BEST AVAILABLE COPY

FIG. 4C

target embedded device  
110A



I/O  
cards  
420

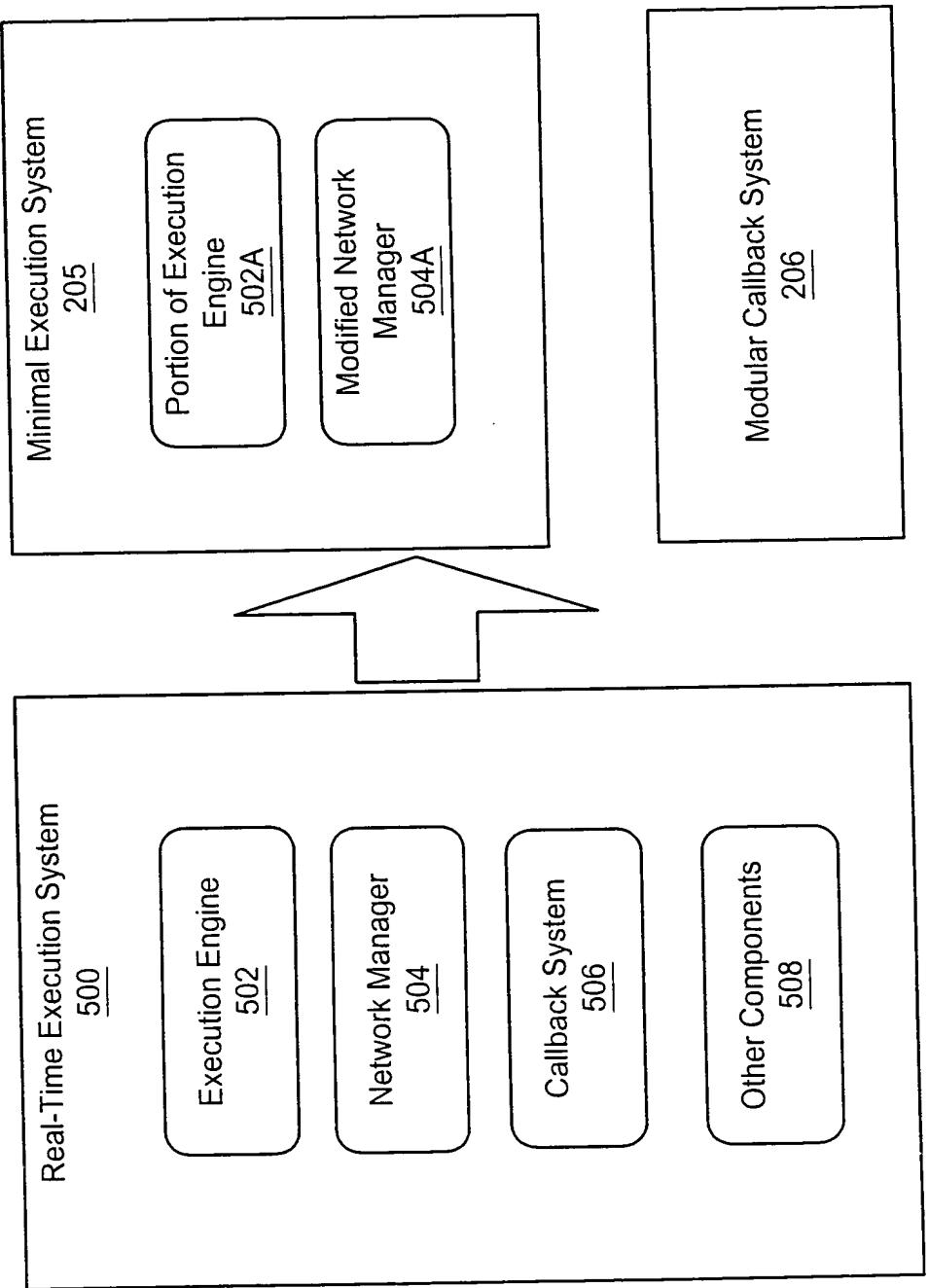


FIG. 5

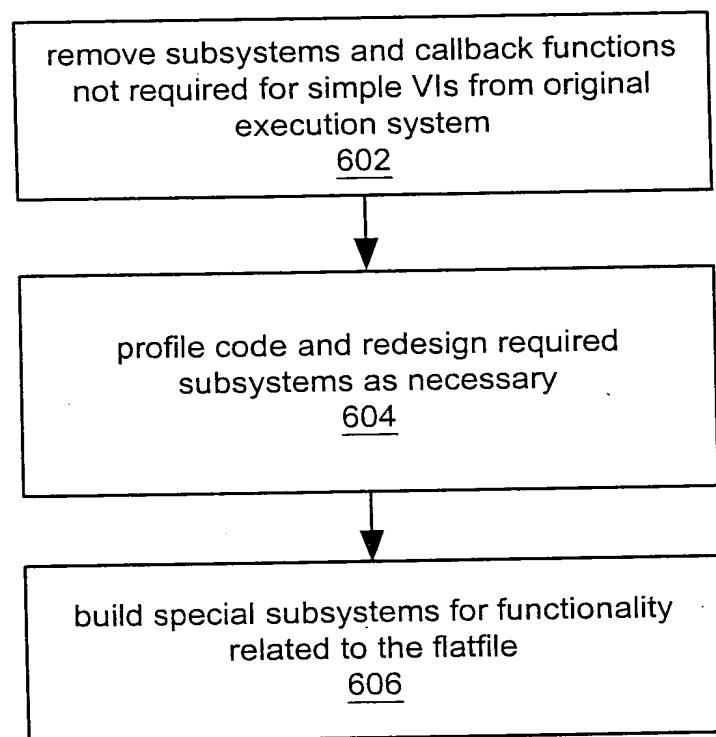
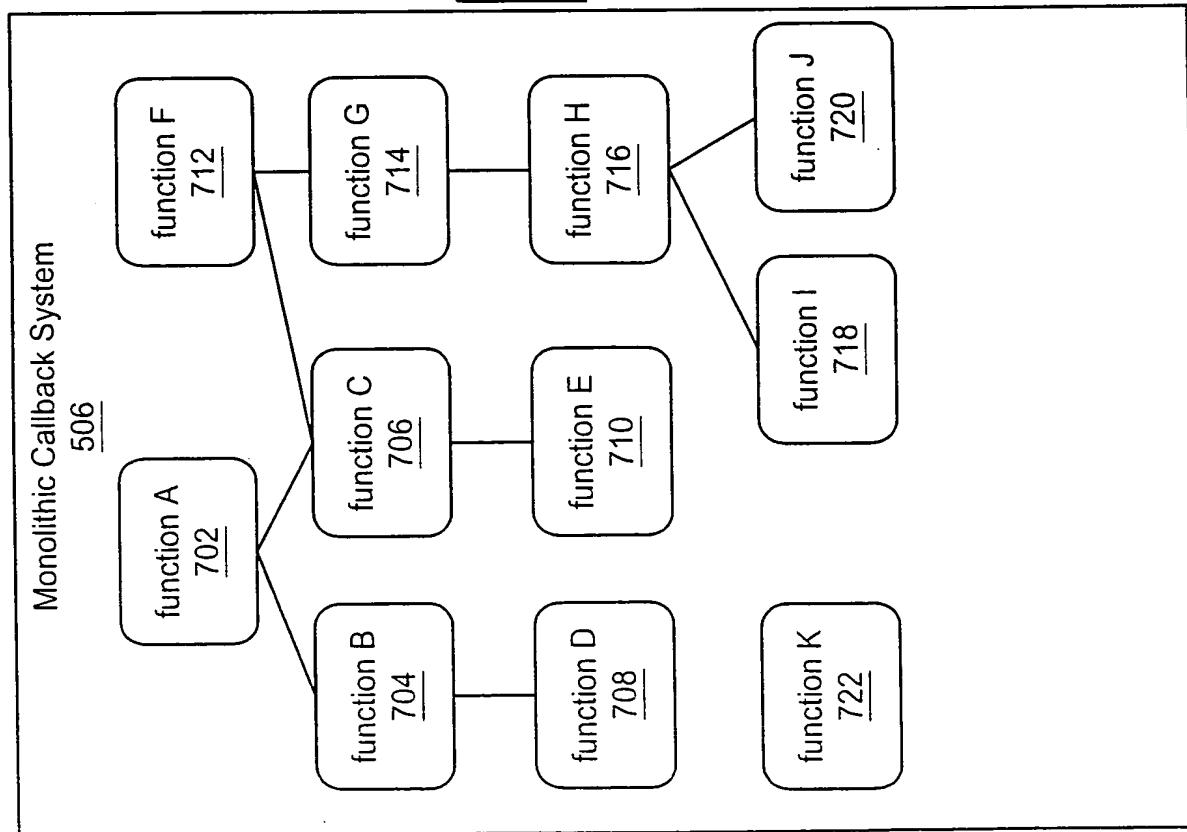
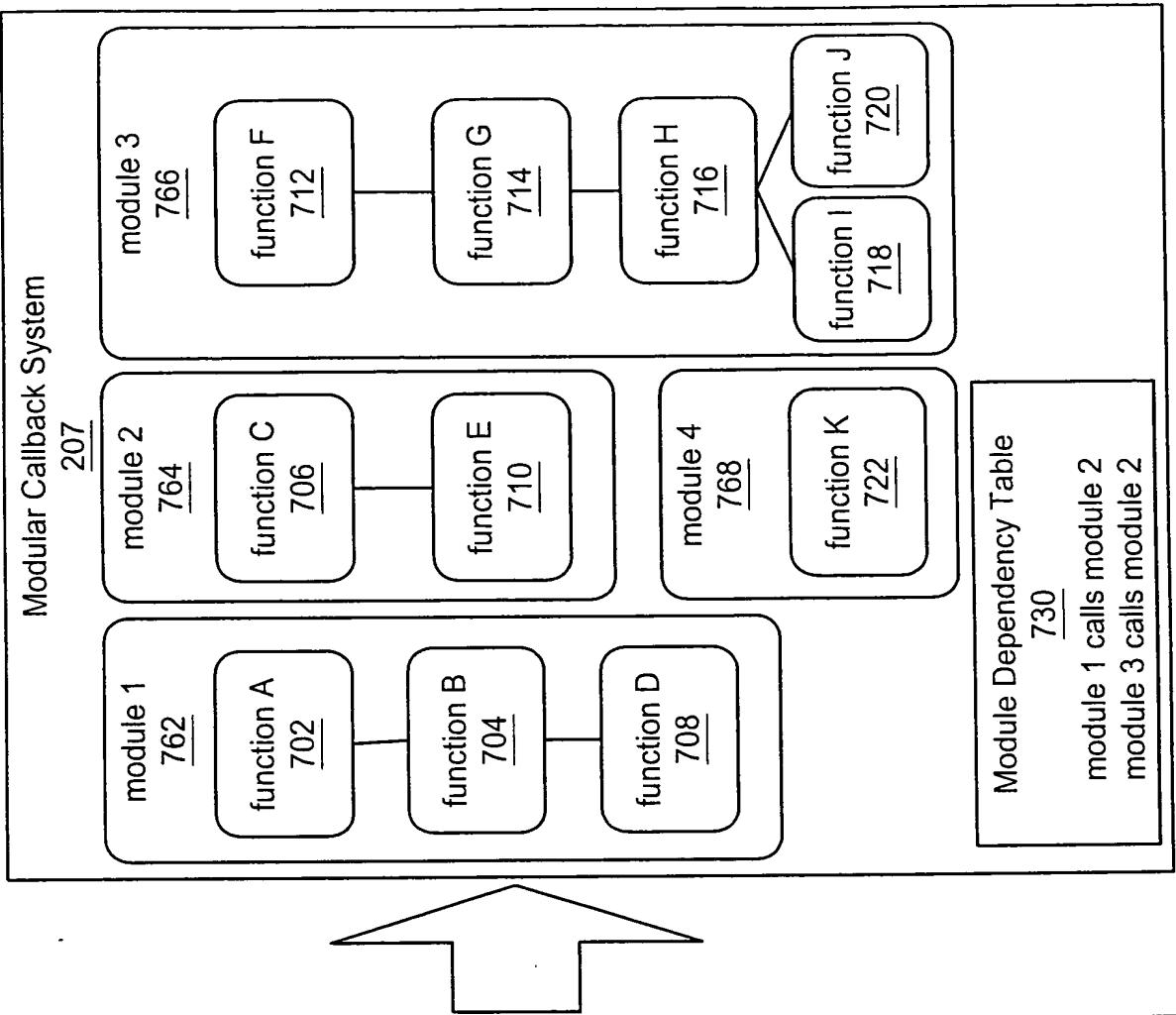


Figure 6

FIG. 7



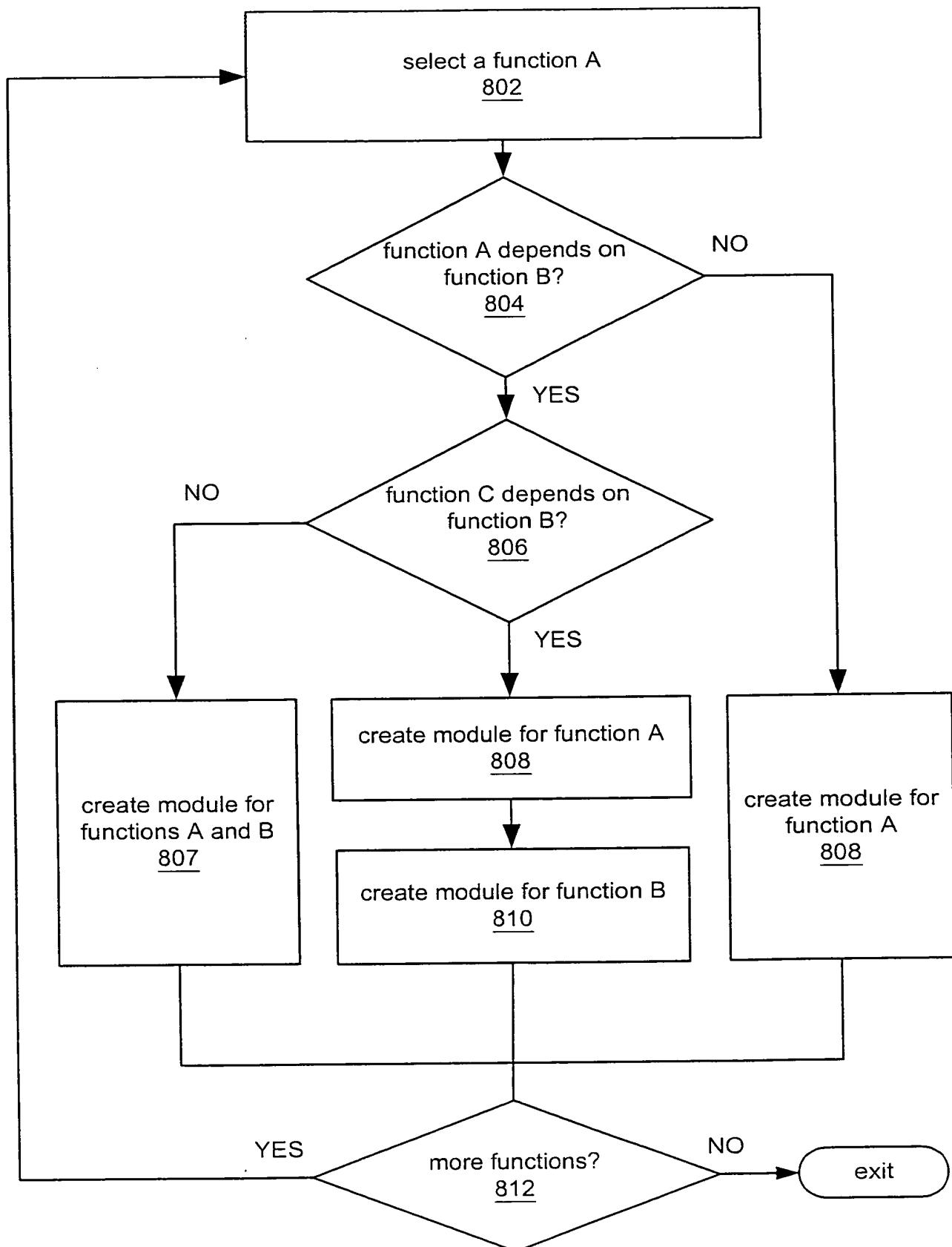
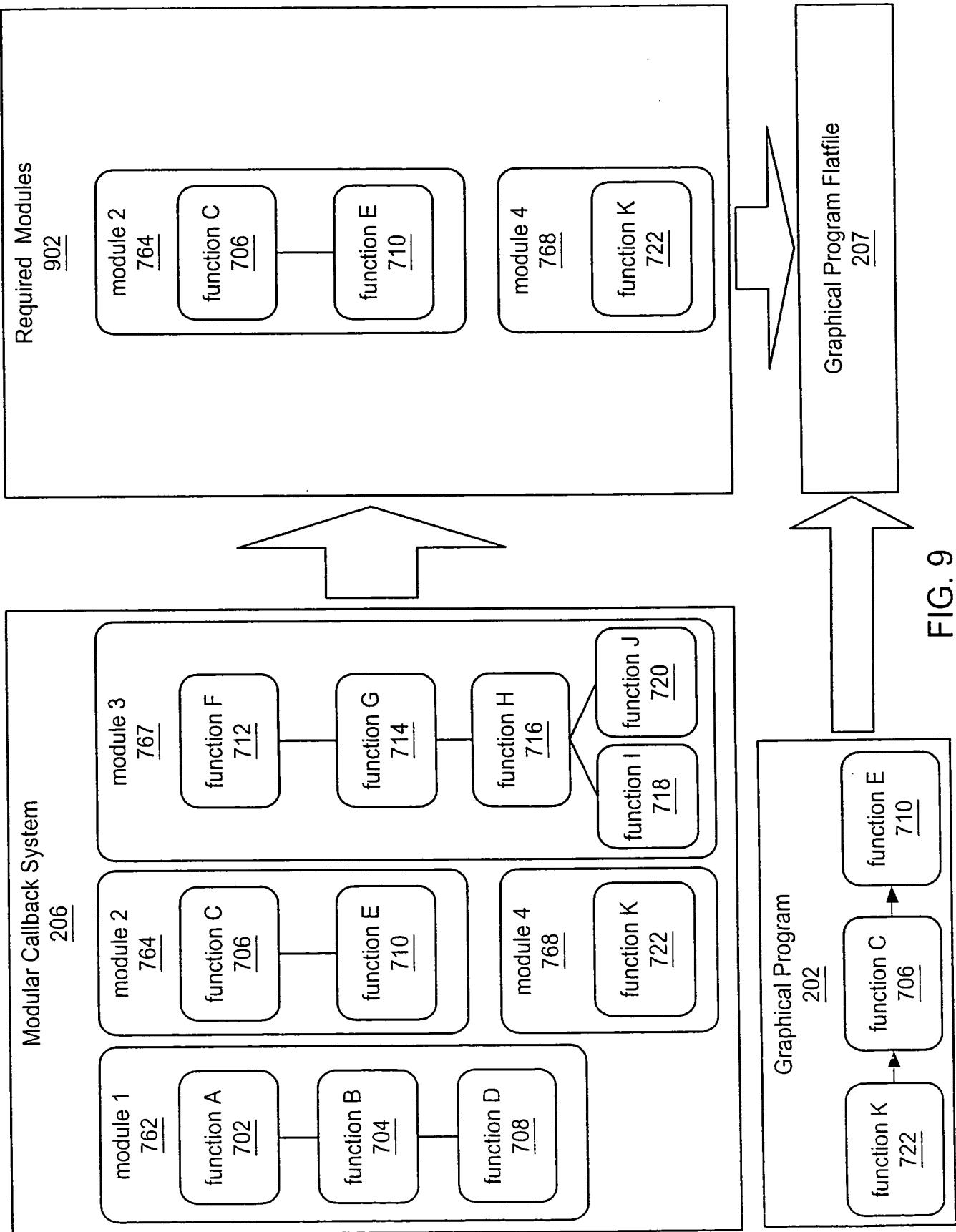


Figure 8



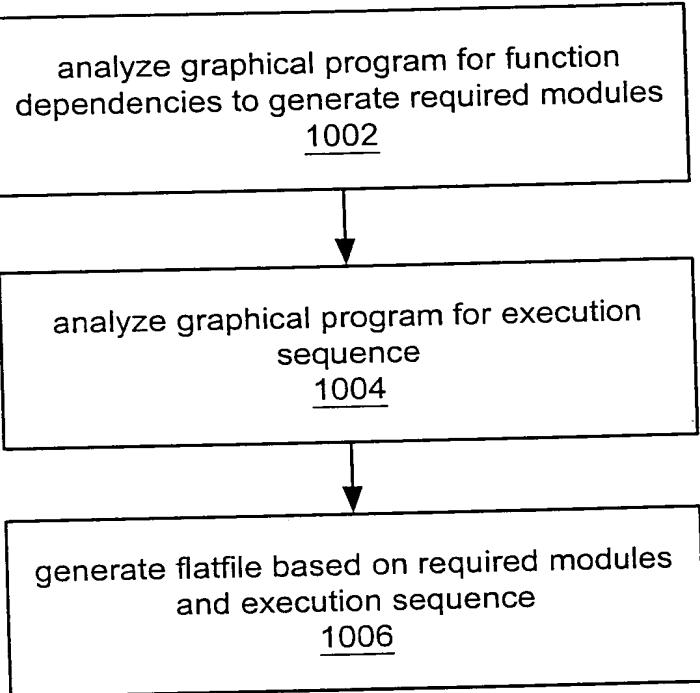


Figure 10

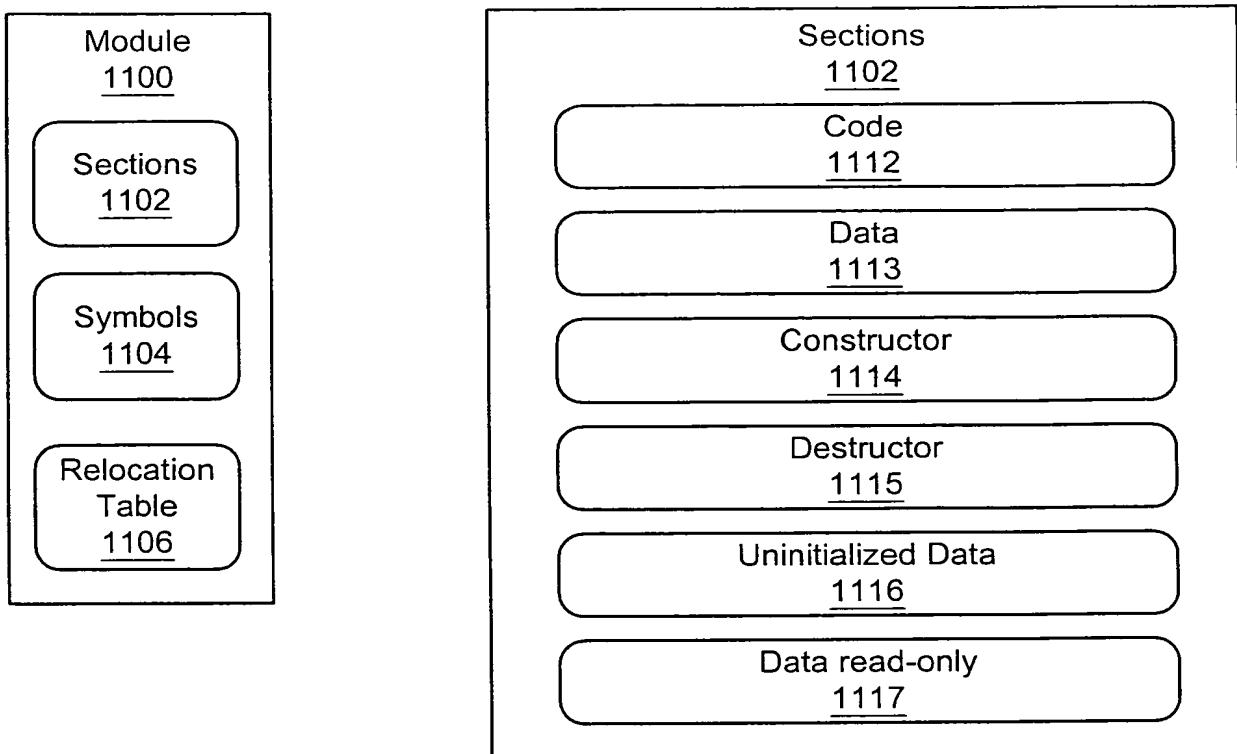
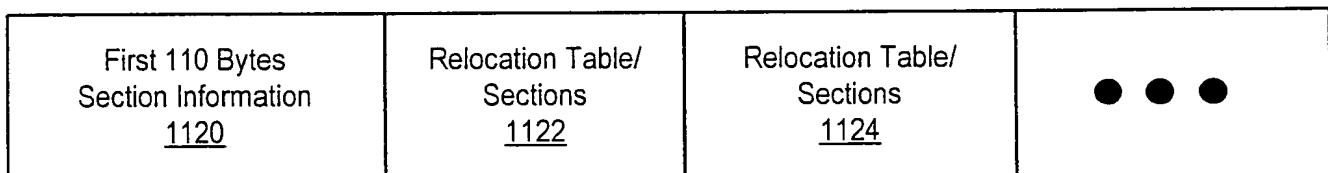


FIG.  
11A



Flatfile  
207

FIG.  
11B

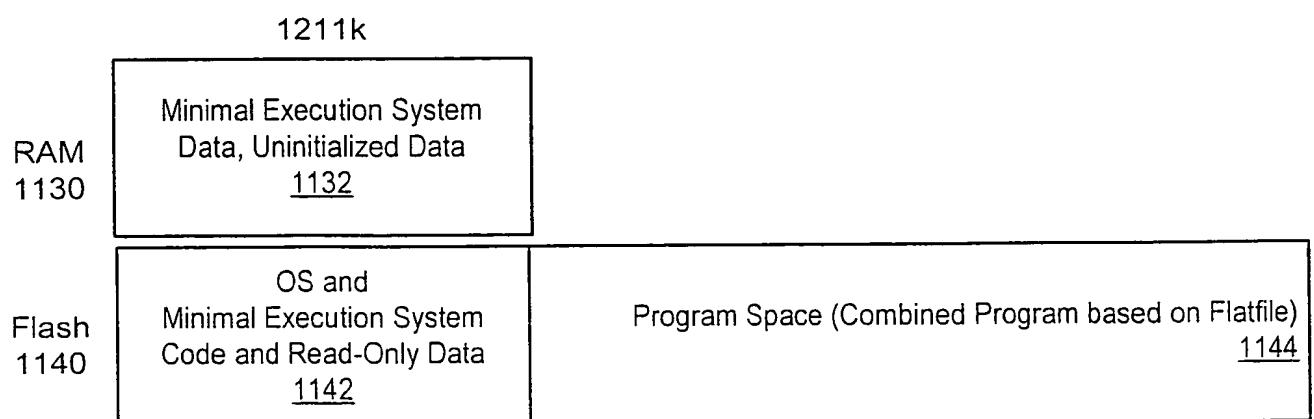


FIG.  
11C

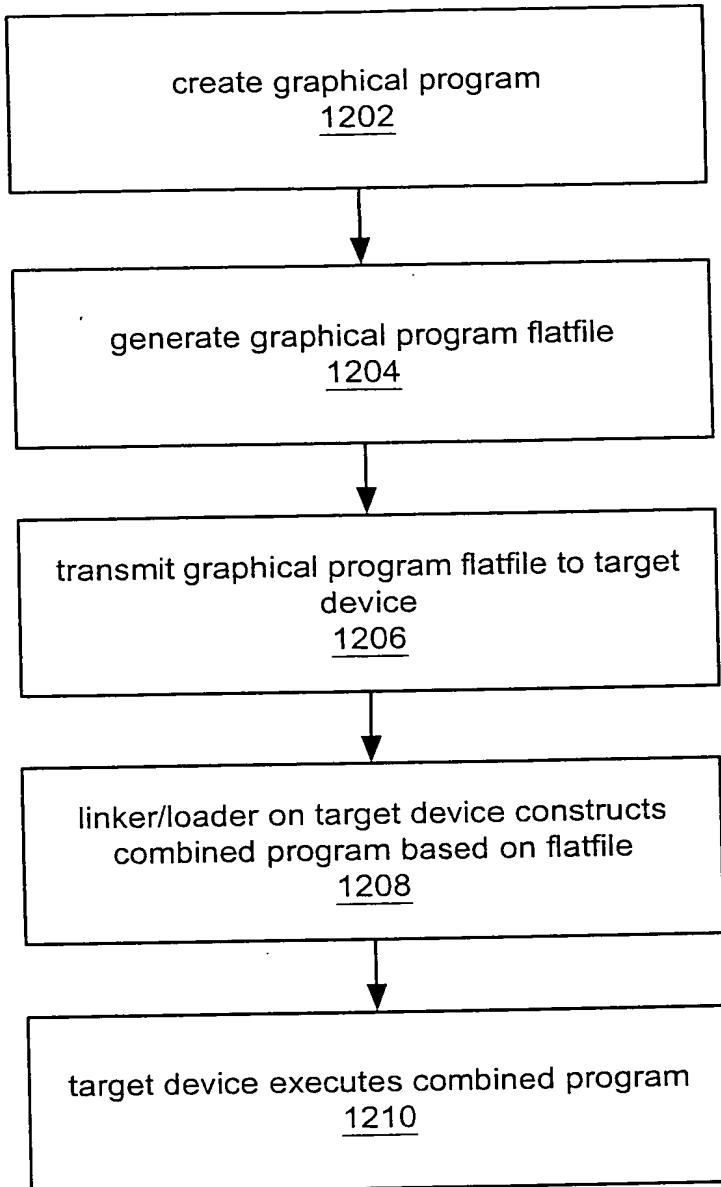


Figure 12

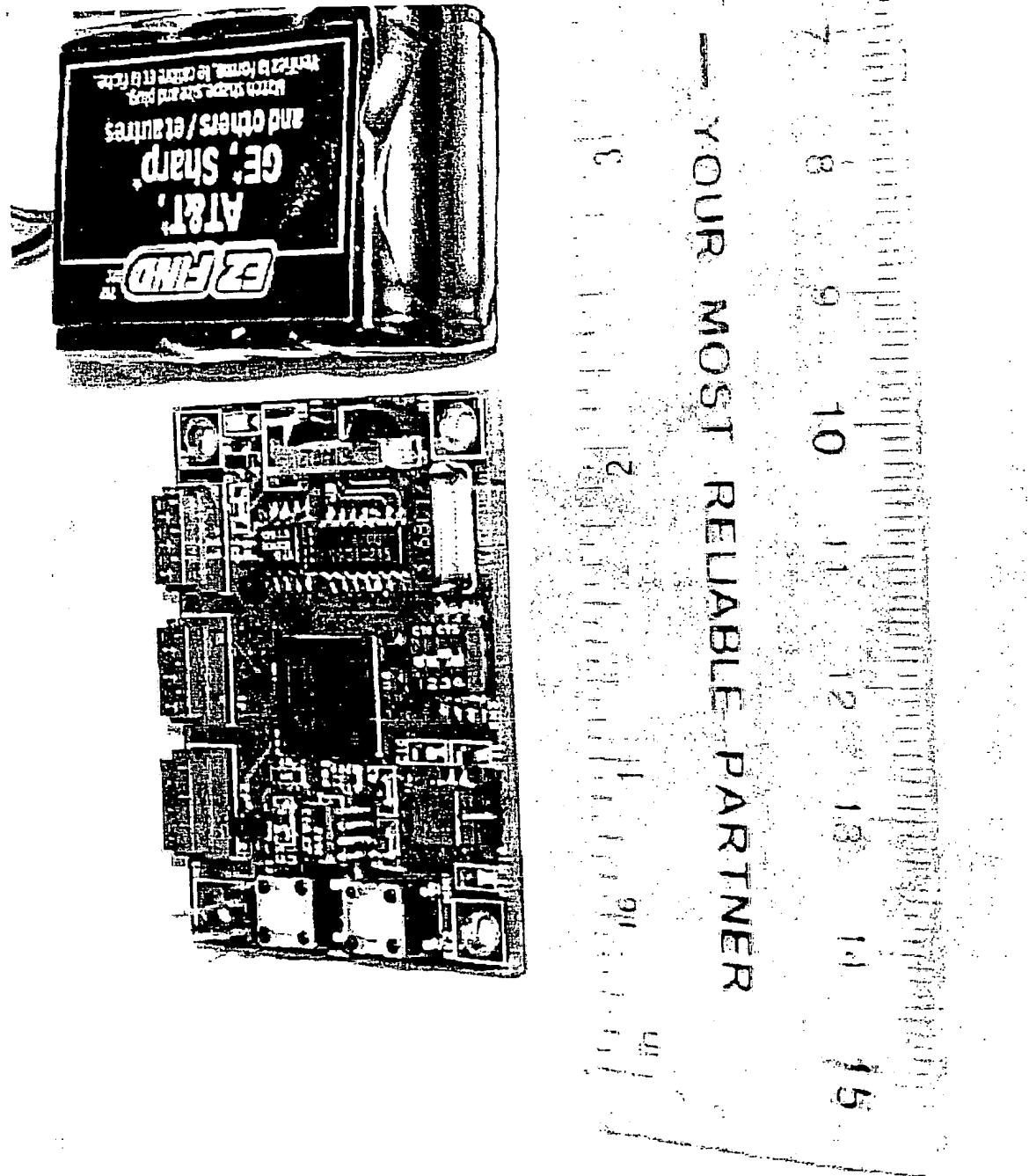


FIG. 13A

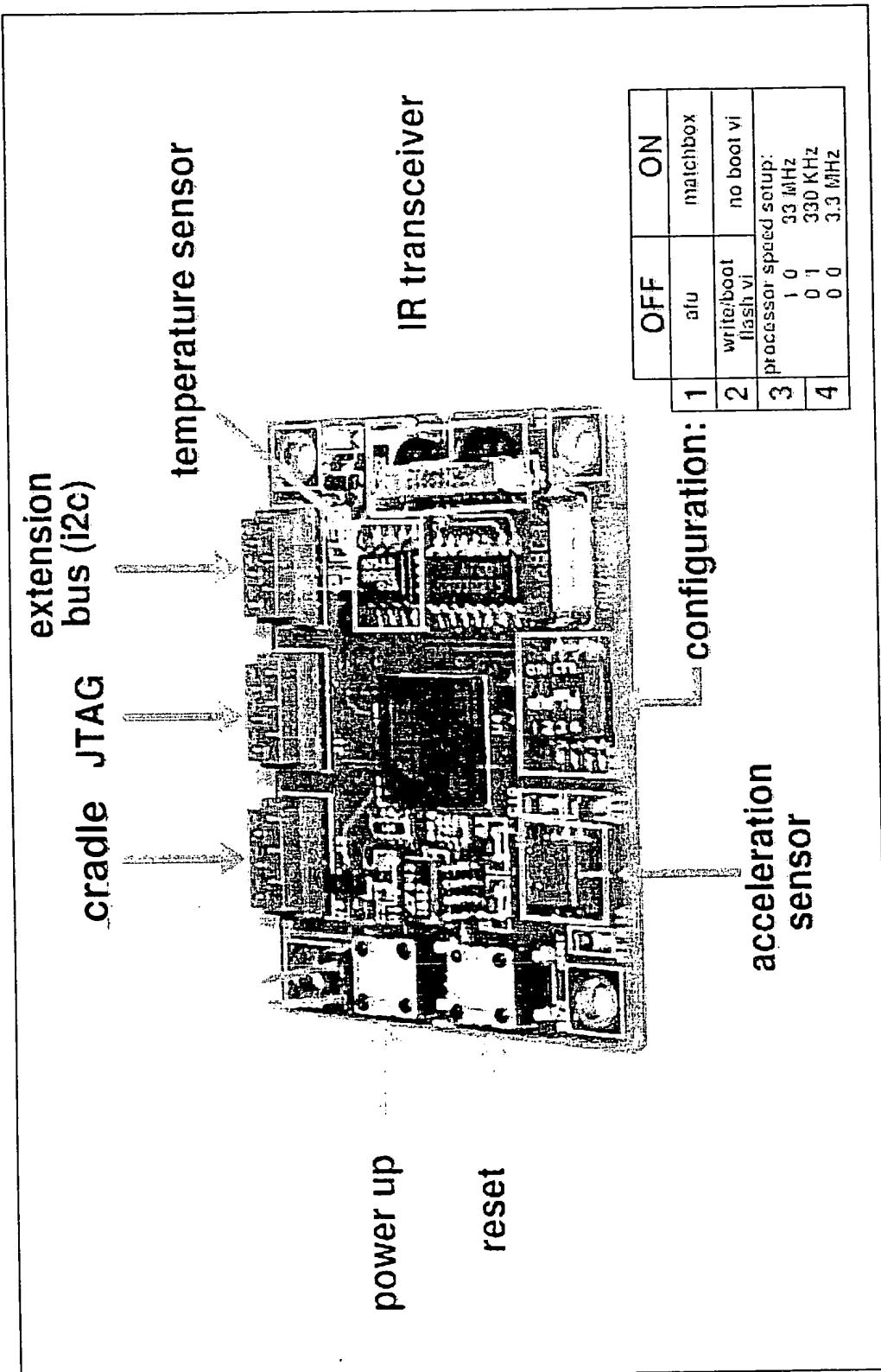


FIG. 13B

FIG. 14

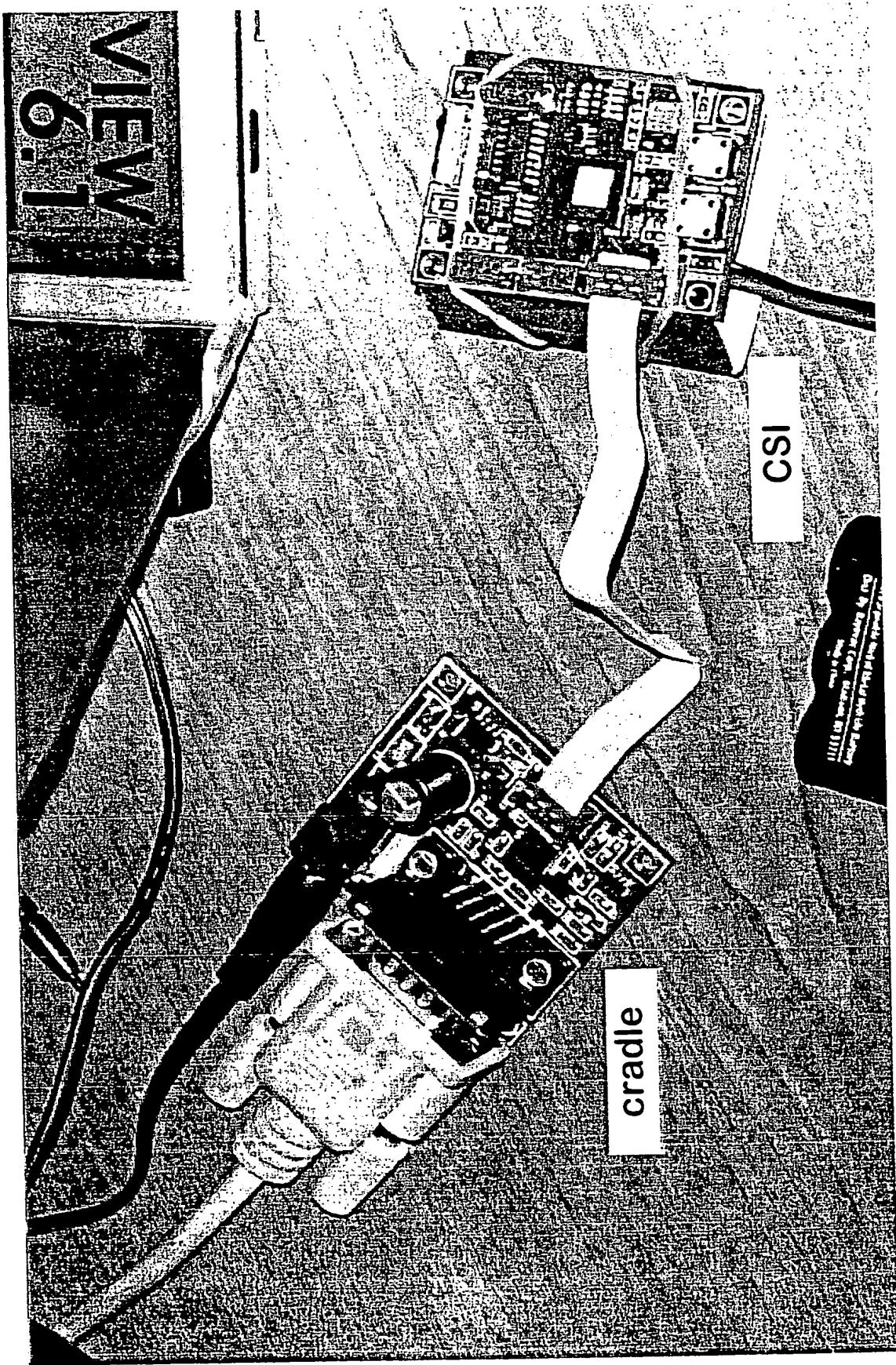
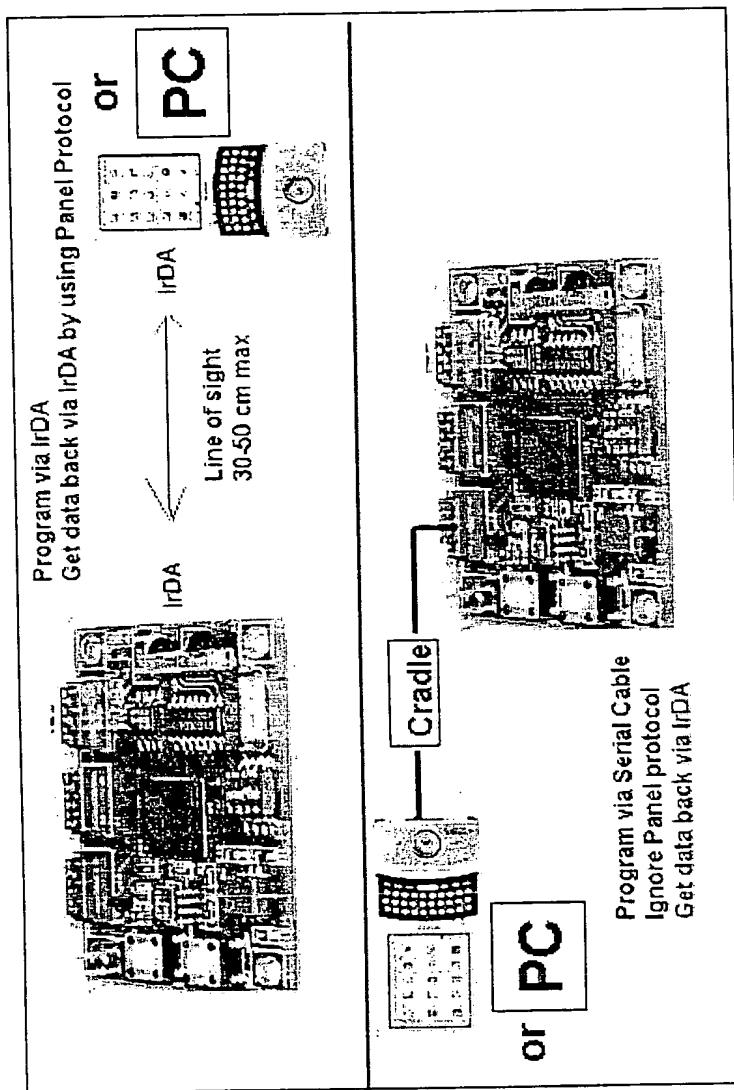


FIG. 15



**afw**  
matchbox symbols  
(9151 bytes)

matchbox  
(141224 bytes)

## running VI's (read-only sections)

stored flat object

Figure 16

FIG. 18

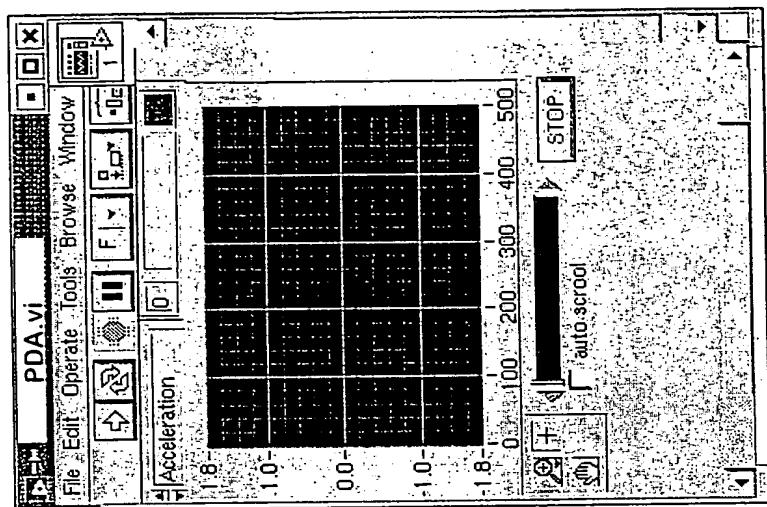
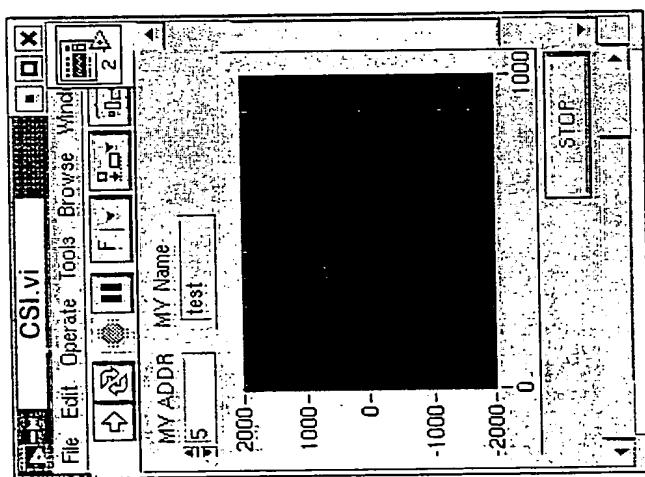


FIG. 17



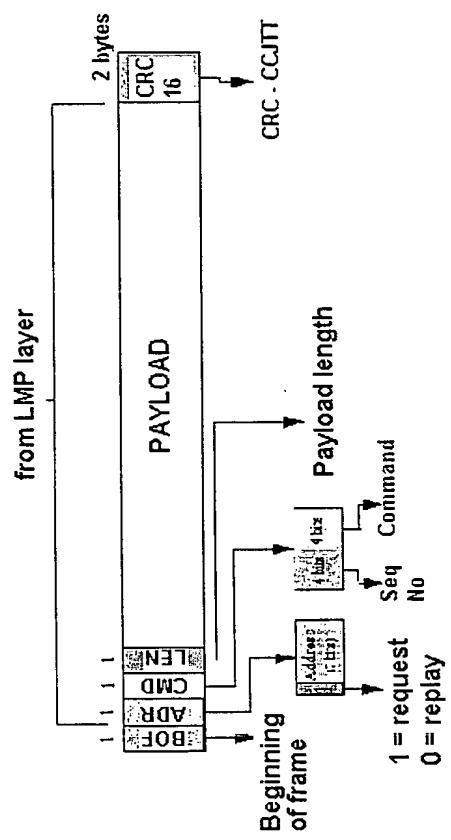


FIG. 19

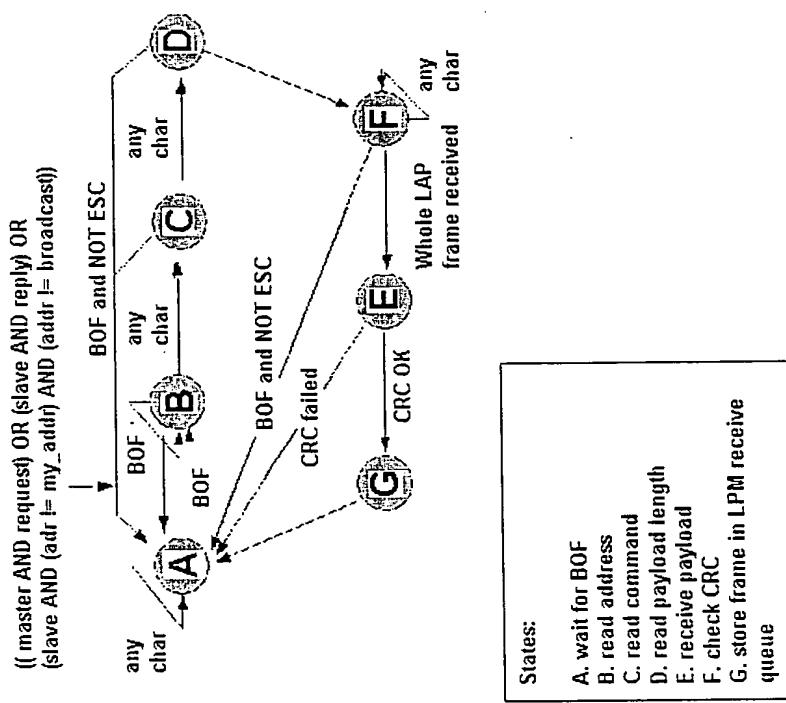
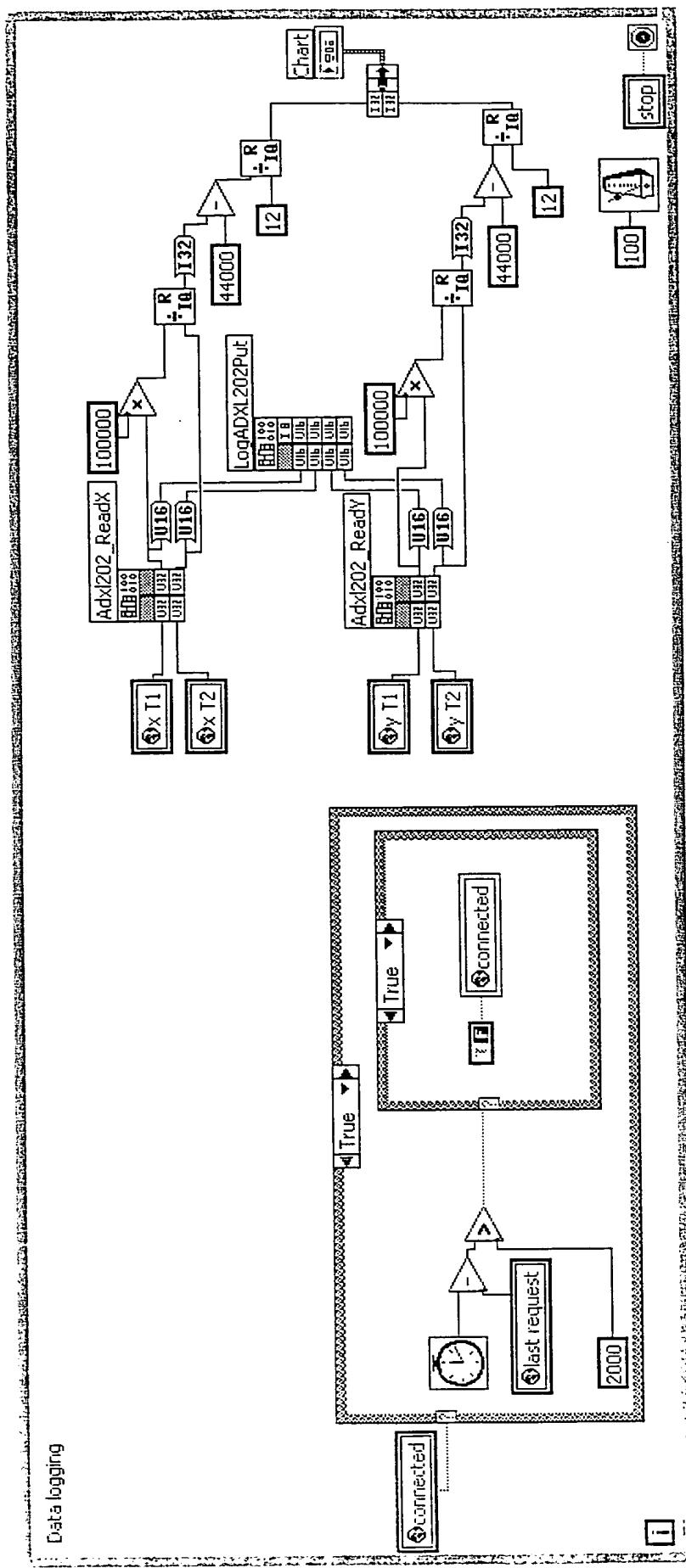


FIG. 20

**Front Panel Protocol communication from CBSI to PDA front panel chart**



**FIG. 21**

FIG. 22A

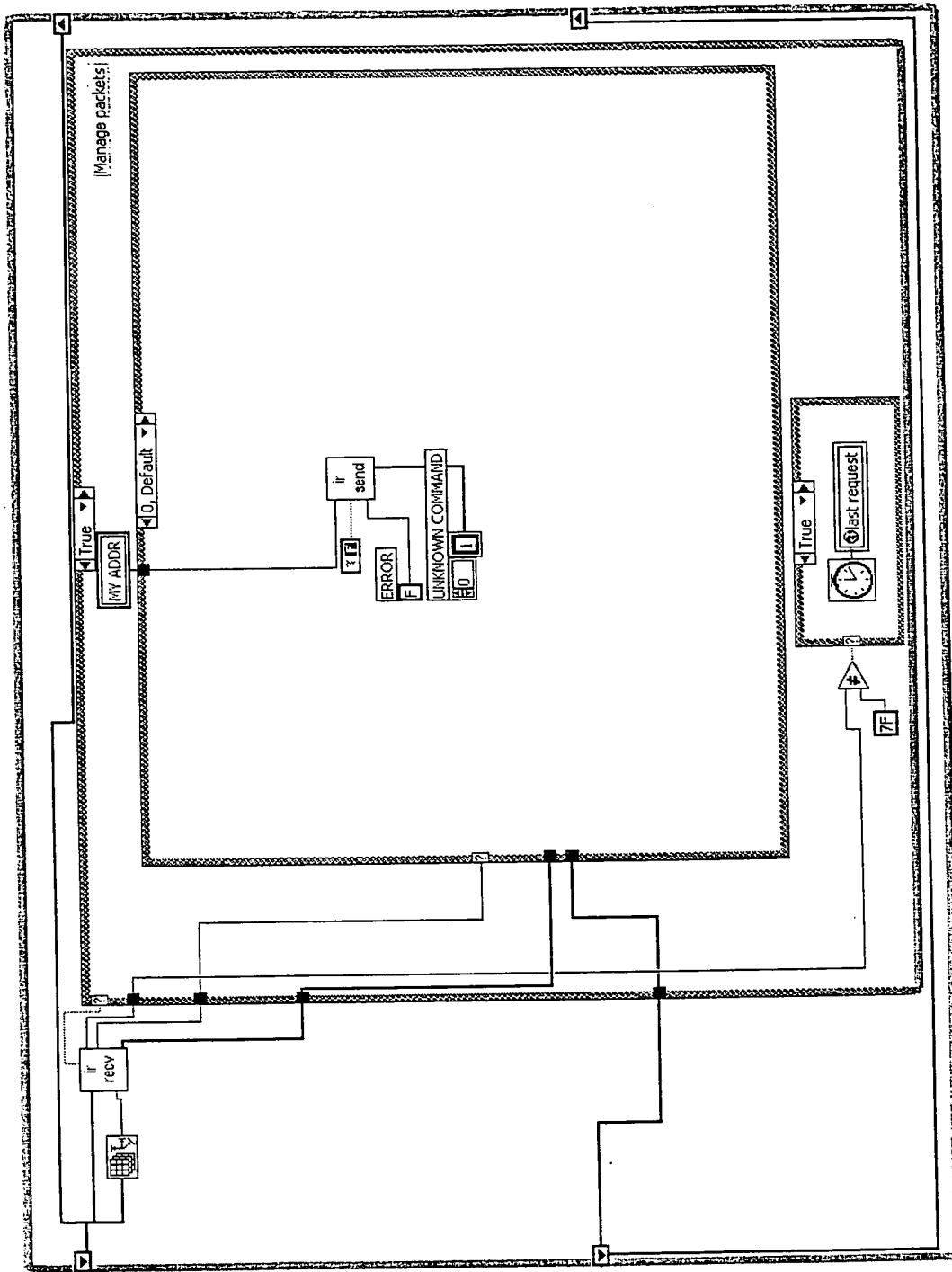


FIG. 22B

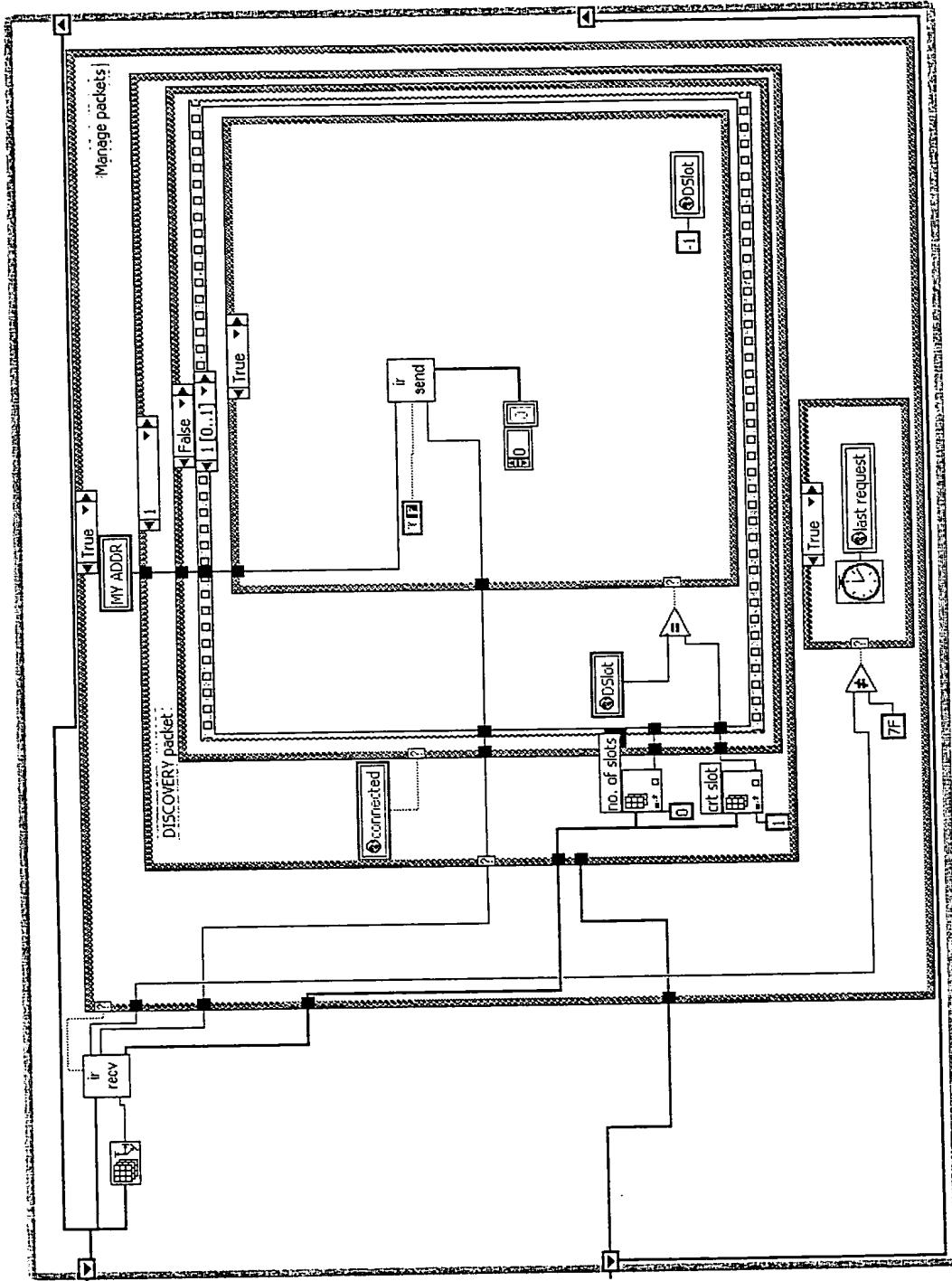


FIG. 22C

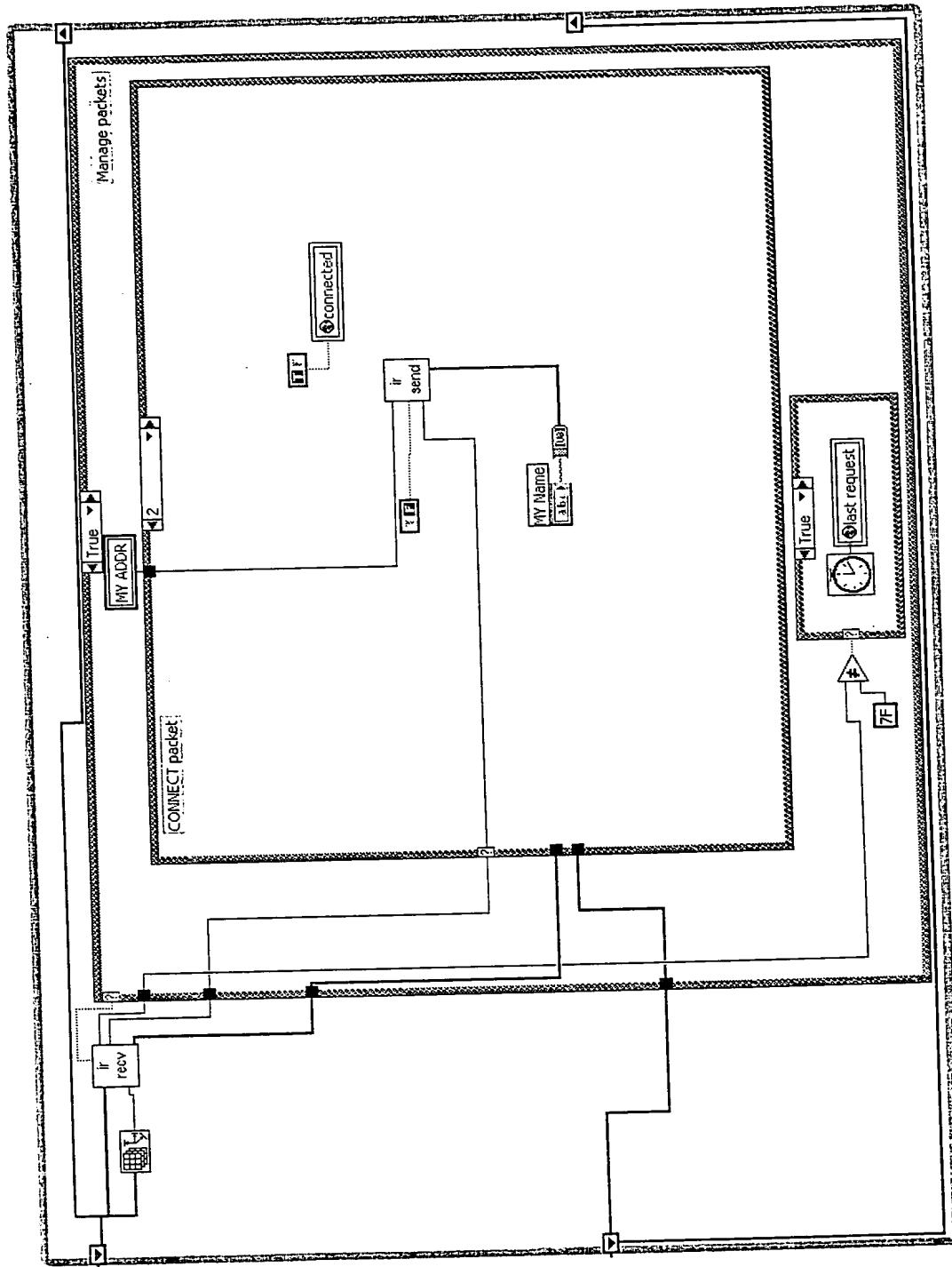


FIG. 22D

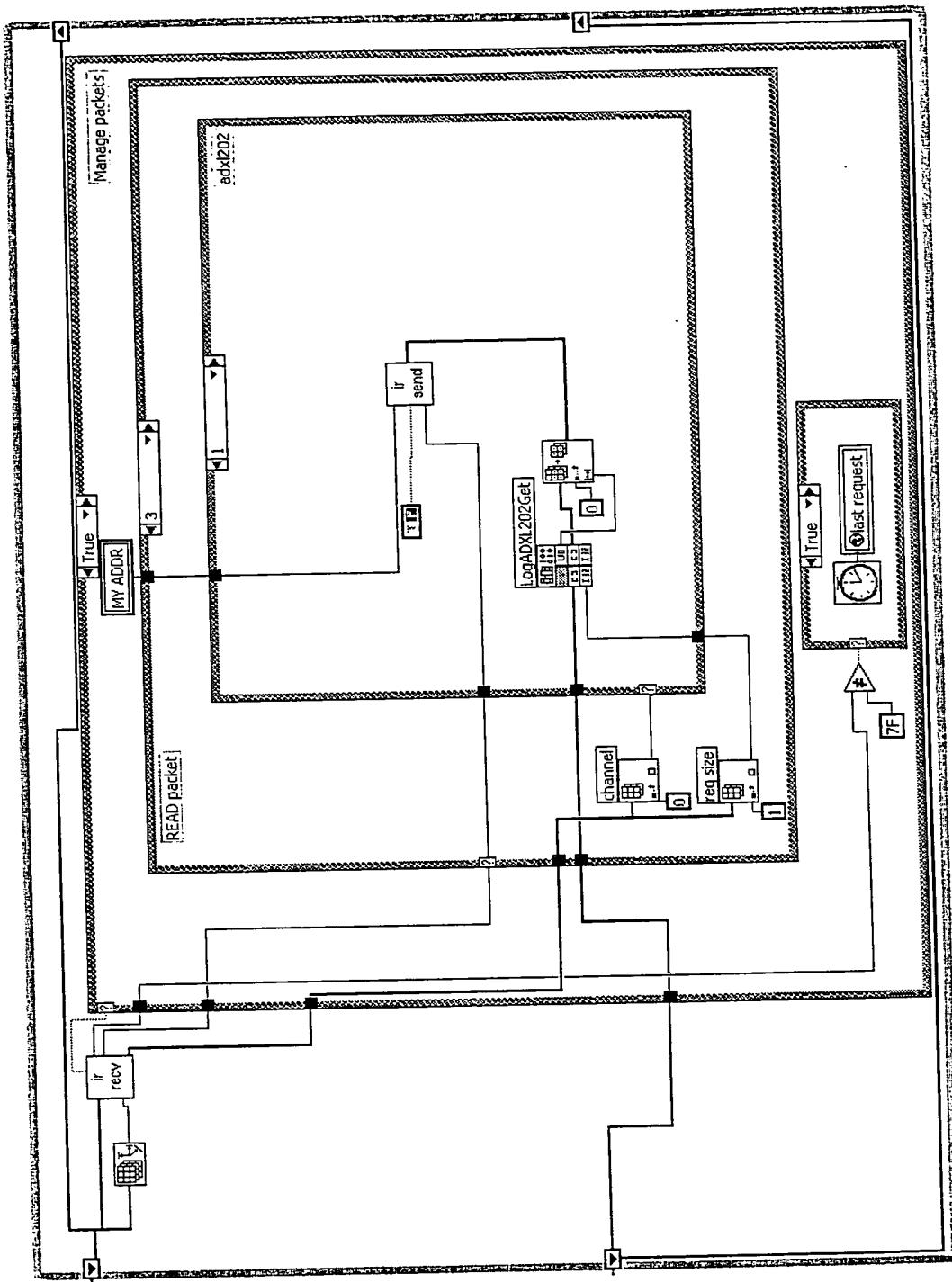


FIG. 22E

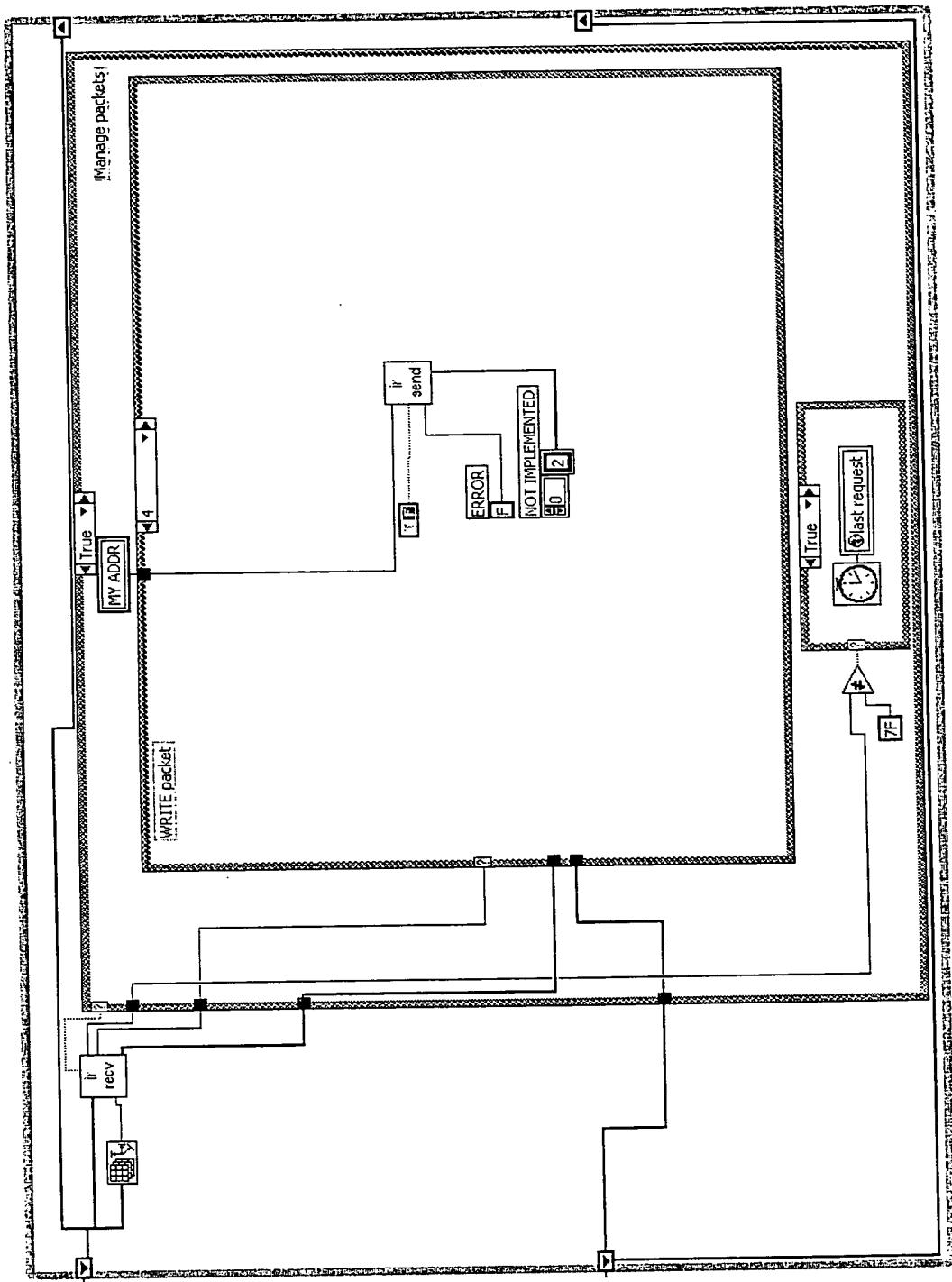


FIG. 23A

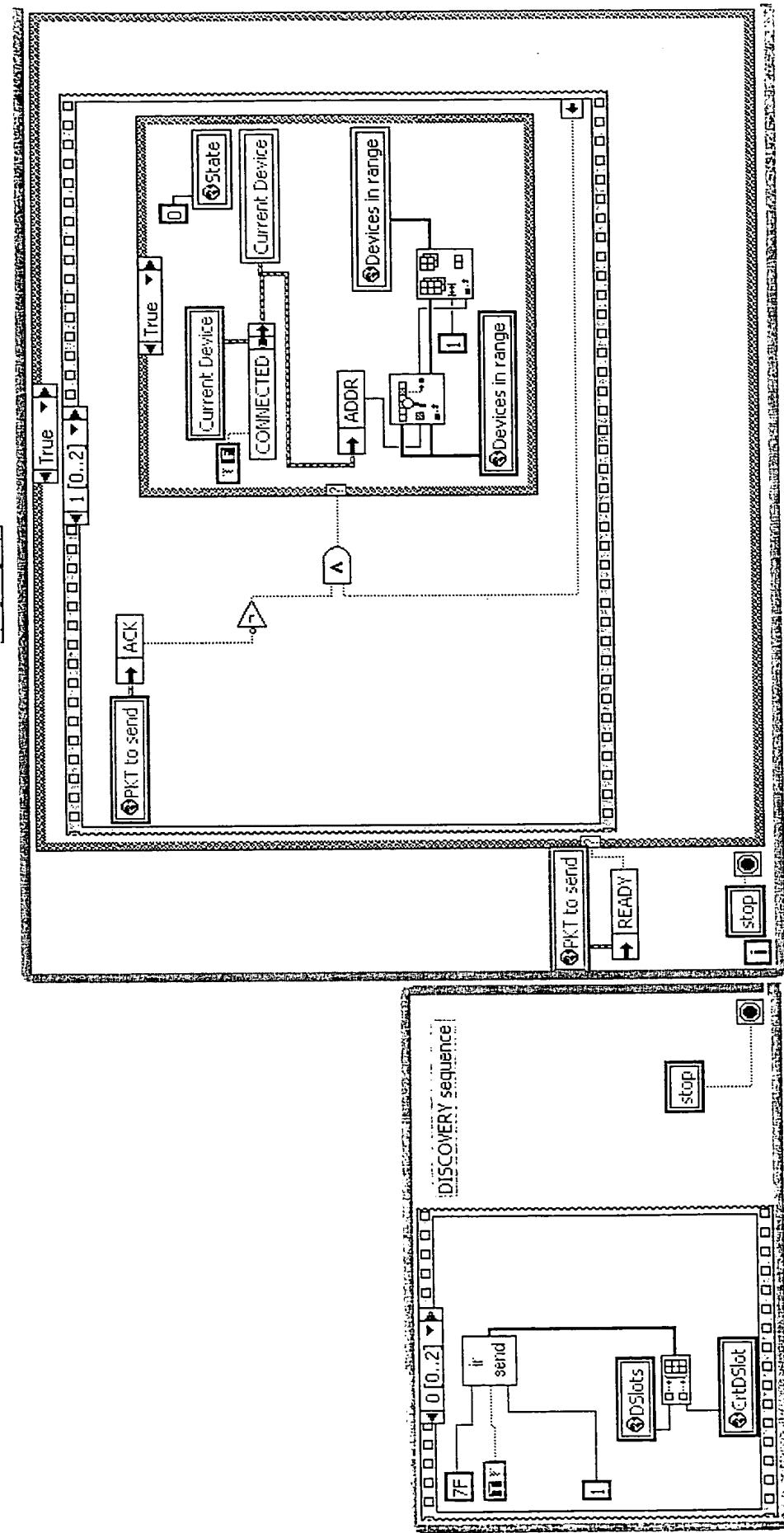
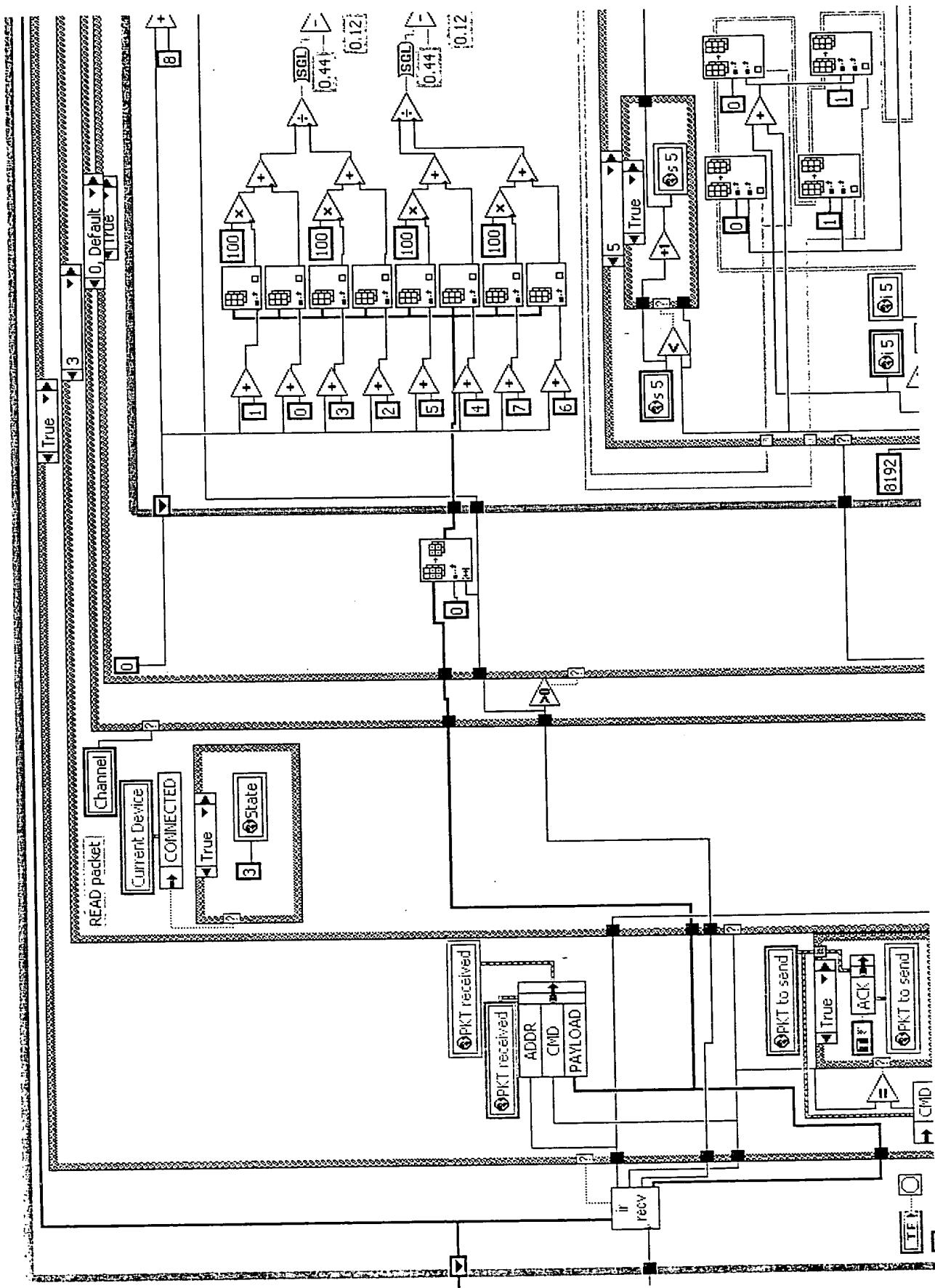


FIG. 23B



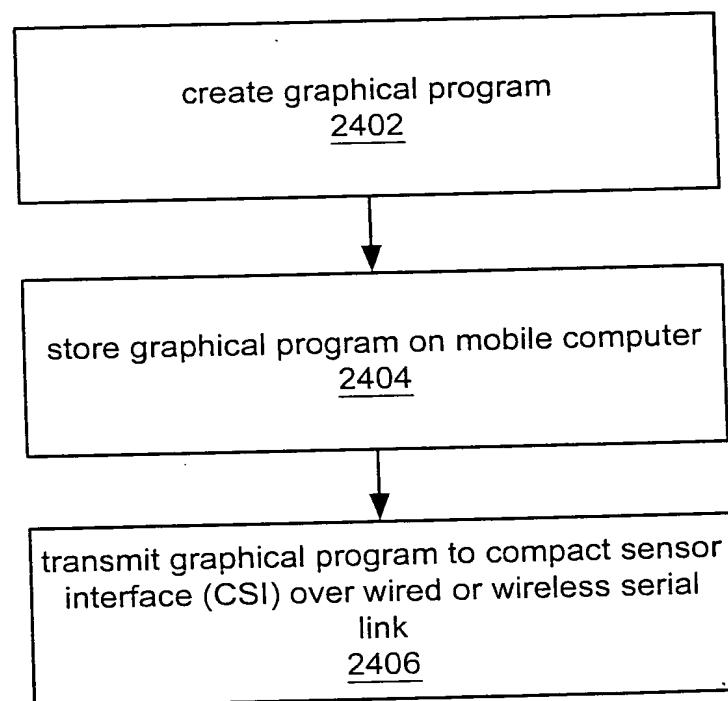


Figure 24

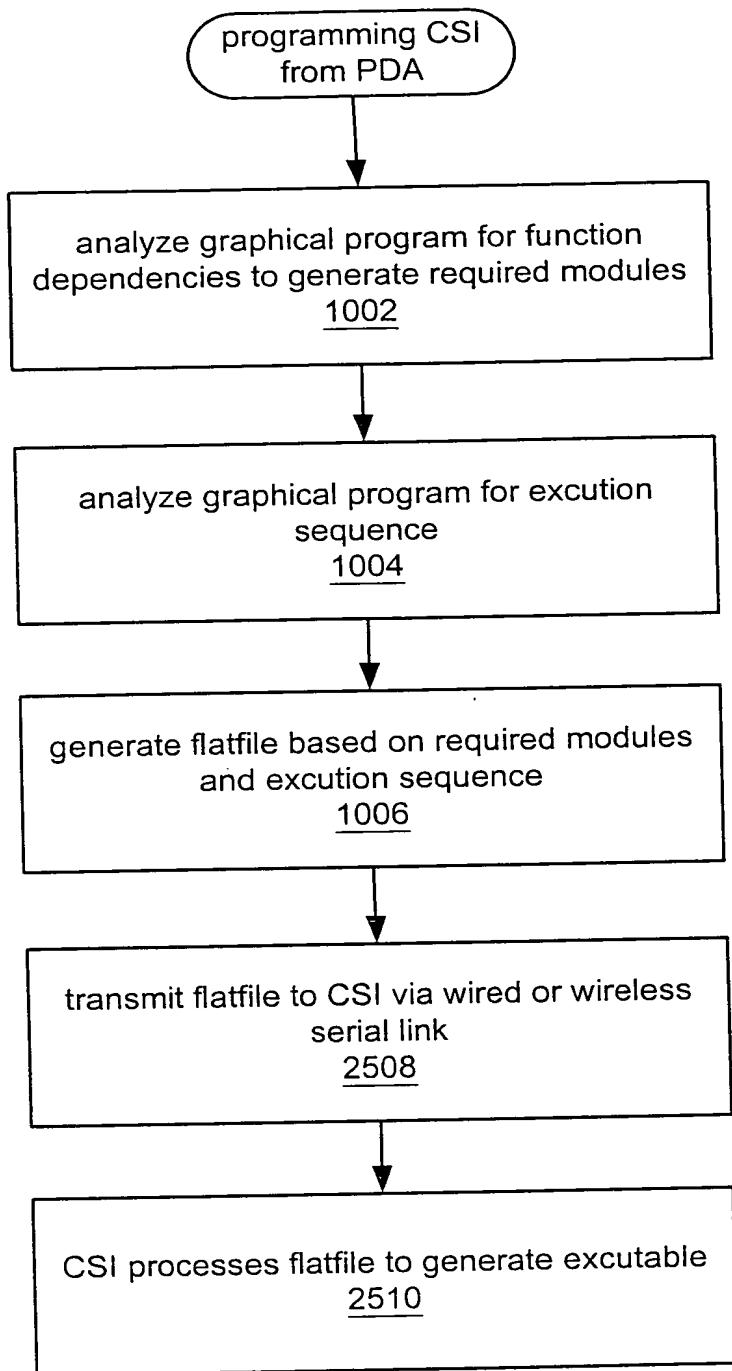


Figure 25

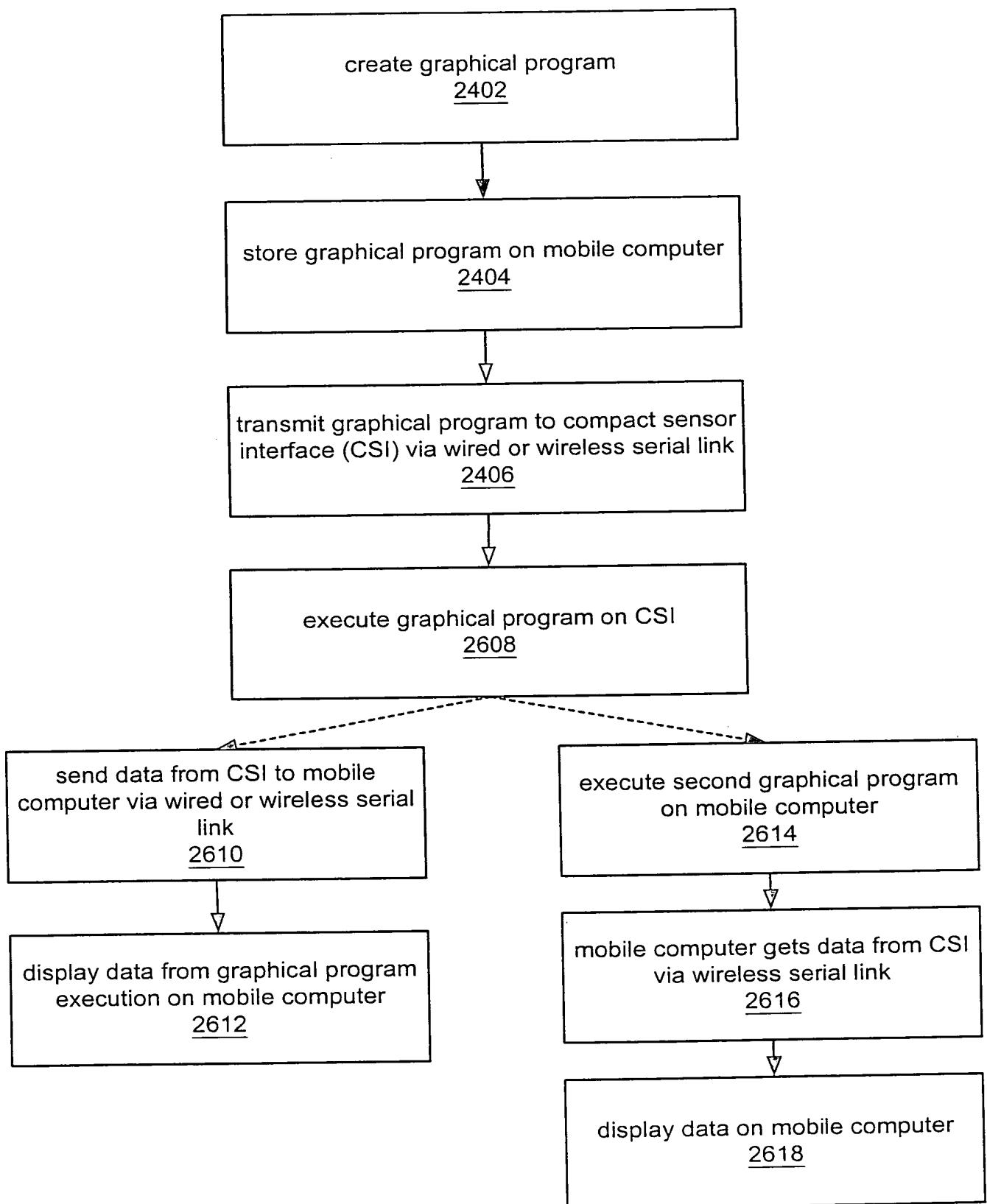


Figure 26